

**EAST Search History****EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	61980	caf acf (conduct\$4 with adhes\$4)	USPAT	OR	OFF	2011/09/10 12:58
L2	61980	caf acf (conduct\$4 with adhes\$4)	USPAT	OR	OFF	2011/09/10 12:58
L3	176624	caf acf (conduct\$4 with adhes\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 12:59
L4	8610	(die chip semiconductor ic (integrated adj circuit) microelectronic electronic) with between with 3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:00
L5	4315	(substrate pcb board carrier) with 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:02
L6	1050	ground\$4 and 5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:02
L7	231757	caf acf (conduct\$4 with (paste adhes\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:04
L8	10275	(die chip semiconductor ic (integrated adj circuit) microelectronic electronic) with between with 7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:05
L9	10336	(die chip semiconductor ic (integrated adj circuit) microelectronic electronic) with (sandwich\$4 between middle) with 7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:05
L10	5054	(substrate pcb board carrier) with 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:05
L11	1179	ground\$4 and 10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERVENT	OR	ON	2011/09/10 13:05

9/10/2011 1:06:01 PM